THE CHINESE UNIVERSITY OF HONG KONG

Micro-Module Courseware Development Grant

Scheme 1: Basic Scheme

<u>Interim Report (2017-18) (Additional Call)</u>

Report due 31 August 2018.
Please return by email to mmcd@cuhk.edu.hk

<u>PART I</u>

Project title: Micro-Modules for Analog and Digital Circuits

Principal supervisor: Marco Ho

Department / Unit: Department of Information Engineering

Project duration: From March 2018 to October 2018

Date report submitted:31 August 2018

1. Project objectives

Is the project on track to meet its objectives?
Have the objectives been changed as a result of the experience of working on your MMCDG project?

The project aims at producing 6 interactive videos for pre-lab preparation and in-lab demonstration for the Electronic Circuit Design Laboratory. The project is on track and is expected to meet the objectives.

2. Progress on process, outcomes or deliverables

What have been accomplished so far?
Have any obstacles been encountered and what are the remaining tasks to be finished?
Is the project still on time for completion (which includes preparation of the final report) on or before the grant expiry date?
Provide a listing of project outputs to date.

Video recordings had been accomplished, and editing has been completed on 4 out of 6 micro-modules. Post-production work such as voice-over and annotation are being done. No obstacles have been encountered so far, and the remaining tasks are anticipated to be completed on time.

3. Evaluation Plan

Have you altered your evaluation plans?
Does your evaluation indicate that you have achieved your objectives?

There is no change to the evaluation plans. Student surveys will be conducted and benchmarking against conventional teaching will be documented when the micro-modules are delivered to the students.

4. Dissemination Activities (reports, websites, video links, products, *etc.*)

Provide a listing of dissemination activities to date.

The interactive webpage and micro-modules will be available to the students in the Electronic Circuit Design Laboratory course.